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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/668,606	09/24/2003	Hyong-Yeul Park	8734.233.00 US	1510
30827 7590 04/08/2008 MCKENNA LONG & ALDRIDGE LLP 1900 K STREET, NW WASHINGTON, DC 20006				
EXAMINER				
DHARIA, PRAJODH M				
ART UNIT		PAPER NUMBER		
2629				
MAIL DATE		DELIVERY MODE		
04/08/2008		PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

1. **Status:** Please all replies and correspondence should be addressed to examiner's new art unit 2629. Receipt is acknowledged of papers submitted on 03-19-2008 under amendments and request for reconsideration, which have been placed of record in the file. Claims 1-18 are pending in this action.

Response to Amended Claims after final

2. Applicant's request for reconsideration and amendments with new claims received on 03-19-2008 are not entered, they do raise new issue and require further consideration and search, since they were not presented before final office action.

Response to Arguments

3. Applicant's arguments filed 03-19-2008 regarding amended claims have been fully considered and they are not persuasive; however, they do require further consideration and search amended claims as they were not presented before mailing of final office action.

4. Applicant argues Lim, Joo Soo (US 20020039089 A1) and Murade, Masao (US 20010022572 A1) fails to disclose a first control signal line in the gate tape carrier packages and a second control signal line in the gate tape carrier packages, the controls the forward and reverse scanning of or selecting of the gate lines sequentially in a display.

Examiner disagrees as Lim, Joo Soo (US 20020039089 A1) discloses a first control signal line formed together with the conductive lines to supply a first control signal to the gate driving integrated circuits (page 1, paragraph 16, Linc1-16, page 2, paragraph 36, paragraph 34, Lines 8,9, page 4, claim 12, figure 3, disclosing controller and control lines received from

controller formed on the TCP controlling gate driving IC mounted on TCP, please also see patent number 7,145,539 of the PGPU document US 2002/0039089 A1, Col. # 1, Line 62 to Col. 2, Line 67, Col. 4, lines 1-65) so that the gate lines of the image display part may be sequentially driven from the first one to the last one (page 1, paragraph 16, page 2, paragraph 36, paragraph 34, Lines 8,9, page 4, claim 1, Lines 1-9, claims 7,12 please also see patent number 7,145,539 of the PGPU document US 2002/0039089 A1, Col. # 1, Line 62 to Col. 2, Line 67, Col. 3, Lines 6-36, Col. 4, Lines 1-65); a second control signal line formed together with the conductive lines to supply a second control signal to the gate driving integrated circuits so that the gate lines of the image display part may be sequentially driven from the last one to the first one (page 3, paragraph 39, Lines 4-10, page 4, claims 1,12); and a first controller to supply the first and second control signals to the first and second control signal lines (page 1, paragraph 16, page 3, paragraph 39, Lines 4-10, page 4, claims 1,12).

Murade discloses a plurality of conductive lines formed at an outer side of the image display part (please see figures 1,2,4 and 5, page 21, claim 23, page 7, paragraphs 54-57, page 17, paragraph 159) of the first substrate (TFT substrate is first substrate which also has scanning lines page 8, paragraph 85) to supply gates driving signals (page 1, paragraph 3, Lines 26-29,10-20) to the gate driving integrated circuits to be supplied to the gate lines (page 1, paragraph 3, Lines 26-29,10-20). Therefore combination does disclose applicant's claimed invention.

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prabodh M. Dharia whose telephone number is 571-272-7668. The examiner can normally be reached on M-F 8AM to 5PM.
6. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.
7. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

/PRABODH DHARIA/

AU2629

Prabodh Dharia

Primary Examiner

April 1, 2008

